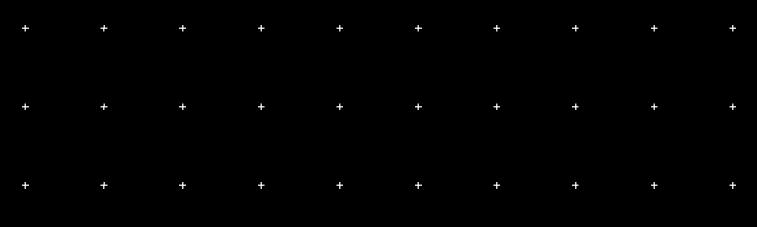


# TF-A Technical Forum

# TF-A and Measured Boot



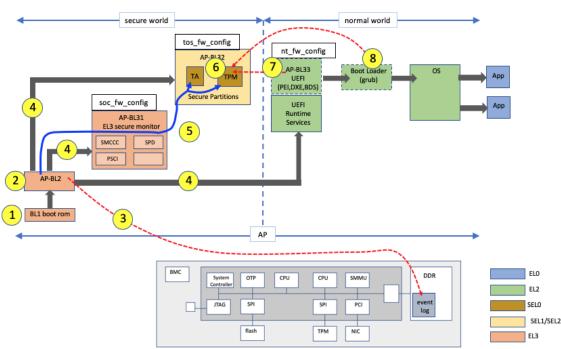
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### **TF-A and Measured Boot**

#### Goals

- Supply a Measured Boot capability to the existing BL1, BL2, BL31, BL32, BL33 boot flow.
- Supply a secure Event Log and APIs that manipulate it to a platform provided TPM solution.
- Supply a fTPM test service to validate the supplied Event Log and API's .
- Provide guidance for platform providers to supply a full Measured Boot solution.

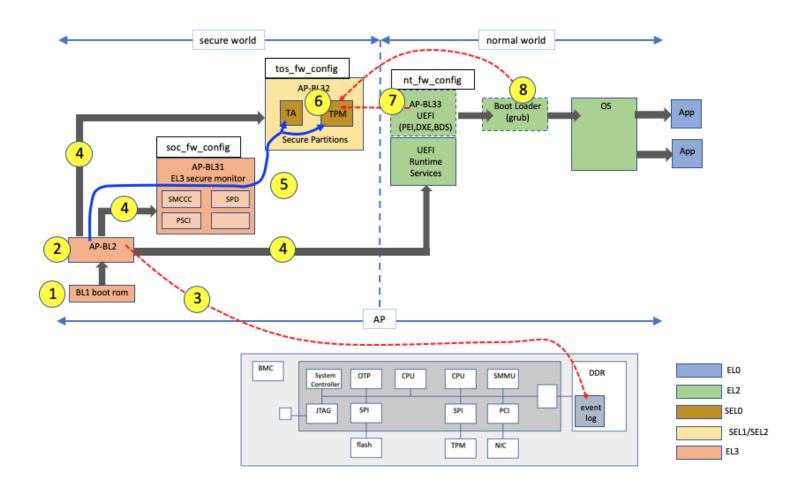


#### Challenges

- TF-A needs to measure BL image boot for a variety of undefined Platform TPM solutions.
- An fTPM test service has been implemented as an OPTEE trusted app based on a number of external Open Source projects (more on this later by Javier)
- Feedback on an initial TF-A implementation had identified a number of possible extensions that have attempted to be addressed in later patches.
  - Upgrading/changing the hash algorithm
  - Requires new API for passing hash algorithm from platform configuration/eFuses
  - Requires new API for passing platform specific 'image\_data'
  - SPM manifest for passing Event Log, CRB region
  - ACPI TPM2 table extension needs SPCI definition
  - Normal world (BL33, OS) to SPCI-based TPM service
  - Secure partition to SPCI-based TPM service

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## **TF-A Measured Boot Flow**



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#### **Boot Flow Steps and Implementation Details**

• SHA256/384/512 hash algorithm for Measured Boot calculations is passed as 'TPM\_HASH\_ALG' build parameter used by Mbed TLS.

1. At reset the boot ROM (BL1 in this example) executes, verifies the first mutable firmware image (BL2), and transfers control to BL2. BL1 has the option of measuring BL2 and passing that measurement as a parameter to BL2.

• *BL1 calculates BL2 image hash and passes these data to BL2 via 'FW\_CONFIG/TB\_FW\_CONFIG' device tree in new 'bl2\_hash\_data' byte array added in 'fvp\_fw\_config.dts for FVP'.* 

2. BL2 performs its normal initialisation and receives its own hash from BL1 image.

• BL2 has an option of measuring itself but by the time Measured Boot driver starts, BL2 image data are changed because of the initialisation of global variables and memory tables.

3. BL2 writes the measurement of BL2 (i.e. itself) to the TPM Event Log in Secure memory.

4. BL2 loads, verifies, and measures each additional firmware image (e.g. BL31, BL32, BL33) along with any critical data (e.g. SOC\_FW\_CONFIG, TOS\_FW\_CONFIG, NT\_FW\_CNFIG) and writes the measurement into the TPM Event Log.

5. BL2 transfers control to BL31, which then initialises and transfers control to the BL32 Secure Partition code.

#### **Boot Flow Steps and Implementation Details**

6. The TPM Secure Partition service initialises. The initialisation process includes initialising the TPM, reading the Event Log and extending all measurements from the log into the TPM.

- Event Log address and size is passed by TOS\_FW\_CONFIG device tree in 2 new added properties:
- Property name: 'tpm\_event\_log\_addr' Value type is an unsigned 64-bit integer specifying the physical address of the event log.
- Property name: 'tpm\_event\_log\_size'
   Value type is an unsigned 32-bit integer specifying the size of the Event Log.

```
/* TPM Event Log Config */
tpm_event_log {
    compatible = "arm,tos_fw";
    tpm_event_log_addr = <0x0 0x0>;
    tpm_event_log_size = <0x0>;
};
```

7. BL2 transfers control to BL33 (UEFI), including passing the address of the Event Log. UEFI measures all drivers and programs (e.g. grub) loaded from external storage and writes them into the TPM (via the SP TPM service) and Event Log.

• Event Log is calculated by BL2 in Secure memory and copied to Non-secure memory.

Address in Non-secure memory is calculated as:

"nt\_fw\_config\_addr + nt\_fw\_config\_max\_size" with values obtained from 'tb\_fw\_config': nt\_fw\_config\_addr = <0x0 0x80000000>; nt\_fw\_config\_max\_size = <0x200>; 5\_\_\_\_\_Confidential © 2020 Arm Limited

#### **Boot Flow Steps and Implementation Details**

TF-A provides Event Log to the BL33 (TFTF/UEFI/U-boot) in 'NT\_FW\_CONFIG' device tree, which address is passed by BL31 as 'arg0' parameter.

8. Bootloader (grub) measures the OS kernel and writes it into the TPM (via the SP TPM service) and Event Log.

#### **Build Options**

- **TPM\_HASH\_ALG**: hash algorithm for Measured Boot calculations used by MbedTLS. Corresponds to HASH\_ALG values "sha256" (default), "sha384", "sha512" used by Trusted Board Boot.
- EVENT\_LOG\_SIZE: Event Log length in bytes, default 1024. FVP booting TFTF with 8 recordered events: sha256: 486; sha384: 598; sha512: 710 bytes.

• EVENT\_LOG\_LEVEL: chooses the log level for Measured Boot driver. See LOG\_LEVEL values, default is 40 (LOG\_LEVEL\_INFO)

#### Platform specific details

"BL\_2" "BL 31"

"BL\_32"

"BL 33"

"HW CONFIG"

"NT FW CONFIG"

"SOC FW CONFIG"

"TOS FW CONFIG"

#define BL2 STRING #define BL31 STRING #define BL32 STRING #define BL33\_STRING #define HW CONFIG STRING #define NT FW CONFIG STRING #define SOC FW CONFIG STRING #define TOS FW CONFIG\_STRING

typedef struct {

unsigned int id; const char \*name; unsigned int pcr; } image data t;

typedef struct { const image data t \*images data; int (\*set\_nt\_fw\_info)(uintptr\_t config base, #ifdef SPD opteed

uintptr t log addr,

#endif

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size t log size, uintptr t \*ns log addr); int (\*set tos fw info)(uintptr t config base, uintptr t log addr, Confidential © 2020 Arm Limited size\_t log\_size); } measured boot data t;

#### Platform specific details

```
/* Platform's table with platform specific image IDs, names and PCRs */
static const image_data_t plat_images_data[] = {
          { BL2_IMAGE_ID, BL2_STRING, PCR_0 }, /* Reserved for BL2 */
          { INVALID ID, NULL, (unsigned int)(-1) }
                                                       /* Terminator */
};
static const measured boot data t plat measured boot data = {
          plat images data,
          NULL, /* platform set nt fw info */
          NULL /* platform set tos fw info */
};
/*
* Function returns pointer to platform's measured_boot_data_t structure
*
* Must be overridden in the platform code
*/
#pragma weak plat get measured boot data
const measured boot data t *plat get measured boot data(void)
{
          return &plat measured boot data;
```

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#### **FVP** example

/\* FVP table with platform specific image IDs, names and PCRs \*/ static const image data t fvp images data[] = { { BL2 IMAGE ID, BL2 STRING, PCR 0 }, /\* Reserved for BL2 \*/ { BL31 IMAGE ID, BL31 STRING, PCR 0 }, { BL32 IMAGE ID, BL32 STRING, PCR 0 }, { BL33 IMAGE ID, BL33 STRING, PCR 0 }, { HW\_CONFIG\_ID, HW\_CONFIG\_STRING, PCR\_0 }, { NT FW CONFIG ID, NT FW CONFIG STRING, PCR 0 }, { SOC FW\_CONFIG\_ID, SOC\_FW\_CONFIG\_STRING, PCR\_0 }, { TOS FW CONFIG ID, TOS FW CONFIG STRING, PCR 0 }, { INVALID ID, NULL, (unsigned int)(-1) } /\* Terminator \*/ }; static const measured boot data t fvp measured boot data = { fvp images data, arm set nt fw info, arm set tos fw info }; /\* \* Function returns pointer to FVP plat\_measured\_boot\_data\_t structure \*/ const measured boot data t \*plat get measured boot data(void) { return & fvp measured boot data;

#### Feedback and future work

"Would be good if the hash alg comes from the config file. This will make the implementation "crypto agile" from the very beginning. It is common to want to upgrade/change the hash algorithm and since BL1 is in ROM, you potentially break measured boot on old devices in case a hash algorithm is broken. The other option is to get the hash algorithm from the platform, perhaps a platform gets it from eFuses as opposed to config files."

• Upgrading/changing the hash algorithm in the run time requires building TF-A with SHA512 increasing the size of Mbed TLS internal buffers.

• Requires new API for passing hash algorithm from platform configuration/eFuses to Measured Boot driver.

"Need a provision to support platform specific image id's and image\_data. A FIP can contain platform specific images, that may need to be measured too."

• Implemented.

When using NT\_FW\_CONFIG for passing Event Log, implement flag that specifies whether measurements are recorded in TPM.
Planned.