

TrustedFirmware

OPEN SOURCE SECURE SOFTWARE

Trusted Firmware Community Project





Open Governance Community Project

Reference open source implementation of Secure software for Arm processors across all market segments

Membership open to all

Board

Technical Steering Committee





Current members







OPEN MOBILE PLATFORM







Member Benefits: Highlights



Governing Board seat driving strategic direction and investments (Budget, Marketing Initiatives, explore new investment areas)



Part of Technical Steering Committee driving technical direction of project (Define Release process, Security Incident Handling process, Roadmaps reviews & influence)



Add and maintain platforms in Open CI (Refer to the "Open CI & Board Farm" slide)



Opportunity for close engineering collaboration with other members



Refer "Membership Structure" slide for details on membership tiers and benefits



The Virtuous Circle Of Collaboration!



Current Projects









All market segments



Open CI & Board Farm



Trusted Firmware Security Center

New centralized Security incident process

https://developer.trustedfirmware.org/w/collaboration/security_center/

- Have you found a security vulnerability in Trusted Firmware?
 > Report it here: <u>security@lists.trustedfirmware.org</u>
- Coordinated disclosure with Trusted Stakeholders and ESS
 - <u>https://developer.trustedfirmware.org/w/collaboration/security_center/trusted_stakeholder_registration/</u>
- Per-project security email aliases
 - <u>https://developer.trustedfirmware.org/w/collaboration/security_center/mailing_aliases/</u>



Trusted Firmware-A https://trustedfirmware-a.readthedocs.io/en/latest/

Secure world reference software for all Arm Cortex-A & Neoverse processors across all market segments.

Trusted boot flow and runtime firmware providing standard implementation of Arm specifications:

- SMCCC (SMC Calling Convention)
- TBBR (Trusted Board Boot Requirements)
- PSCI (Power State Coordination Interface)
- SCMI (System Control & Management Interface)
- FF-A (Firmware Framework for A-Profile)

Cortex-A/Neoverse



TF-A-Tests

https://trustedfirmware-a-tests.readthedocs.io/en/latest/

A suite of bare-metal functional tests to exercise TF-A features from the Normal World, without dependencies on a Rich OS

It provides a strong basis for TF-A developers to validate their own platform ports and add their own test cases, interacting with TF-A through its SMC interface

Features currently tested include:

- SMC Calling Convention
- Power State Coordination Interface (PSCI)
- Software Delegated Exception Interface (SDEI)
- Performance Measurement Framework (PMF)
- Trusted Board Boot Requirements (TBBR)
- Secure Partition Manager (SPM) ... and lots more!

Cortex-A/Neoverse



Trusted Firmware-M

Implements the Secure Processing Environment (SPE) for Armv8-M, Armv8.1-M architectures It is the platform security architecture reference implementation aligning with PSA Certified guidelines.

It consists of Secure Boot and a set of Secure Services such as Secure Storage, Crypto, Attestation, Firmware update . for Applications accessible via PSA Functional APIs.

Cortex-M



OP-TEE

A reference implementation of a Trusted Execution Environment (TEE), designed as companion to a nonsecure Linux kernel running on Arm Cortex-A cores using the TrustZone technology.

Implements <u>TEE Internal Core</u> <u>API v1.1.x and the <u>TEE Client</u> <u>APIv1.0, as defined in the</u> <u>GlobalPlatform API</u> specifications.</u>





Mbed TLS

- Portable, highly modular, easy-to-use TLS and X.509 library
- Extensively used in various market segments
- Distributed under Apache2.0 License
- Components
 - Cryptography
 - Protocol (TLS, DTLS)
 - Certificates (X.509, PKI)
- PSA Crypto (Mbed Crypto), derived from Mbed TLS library, brings together Crypto primitives and makes them available via. PSA Crypto APIs.
- PSA Crypto also support driver interfaces to integrate with Secure Elements and Crypto Accelerators.



Trusted Services

- Framework to develop Security related Services
- Deployable over range of Isolated Processing Environments (e.g., Secure ELO Partitions under OP-TEE, Secure Partition under Hafnium.)
- Applications access Trusted Services for Security Operations via. a standardized service layer
- Includes PSA Trusted Services for Cryptography, Storage and Attestation



Arm CCA: Open Source Software enablement

https://connect.linaro.org/resources/arm-cca/



Arm CCA: More resources

- Introducing the TF-A Monitor code for the Arm CCA architecture
- <u>CCA Awakens on Arm's Modelling Platform</u>
- LVC21F-311 Overview of Firmware Architecture for Arm CCA



Membership Structure

*: Only for G2 & G3 General members

G1: \$2.5K (0 to 50 empl. only) G2: \$10k (0-499) G3: \$25k (500+)

	Diamond	Platinum	General	Community (Uni/Non-profit)	Individual (invite only)	Non- Member
Code Access, Review Participation	Yes	Yes	Yes	Yes	Yes	Yes
Technical Forums	Yes	Yes	Yes	Yes	Yes	Yes
Logo and marketing recognition (scaled per tier)	Yes	Yes	Yes	Yes	N/A	No
Technical Steering Committee (TSC) seat + vote	Yes (2 votes each)	Yes (1 vote each)	Yes (1 vote every 5)	Yes (1 vote every 5)	Yes	No
Governing Board seat + vote	Yes (2 votes each)	Yes (1 vote each)	Yes* (1 vote every 5)*	Yes (1 vote every 5)	No	No
Boards in Open CI	2 new / year	1 new / year	No	No	No	No
Fees	\$100k	\$50k	G1: \$2.5K G2: \$10K G3: \$25K	\$2.5K	\$500	No

Additional benefits will be evaluated and revisited for future investment topics (MISRA, LTSs, ...) when it happens



Thank you

Adopt Trusted Firmware to build your next secure platform

Visit www.TrustedFirmware.org or email enquiries@trustedfirmware.org for more information